

<b>Notice of References Cited</b>	Application/Control No. 10/612,836	Applicant(s)/Patent Under Reexamination WEIMER ET AL.	
	Examiner Stephen W. Smoot	Art Unit 2813	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2003/0222318 A1	12-2003	Tanaka et al.	257/406
	B	US-6,114,734	09-2000	Eklund, Robert H.	257/410
	C	US-6,271,125 B1	08-2001	Yoo et al.	438/637
	D	US-6,444,533 B1	09-2002	Lyding et al.	438/308
	E	US-6,521,977 B1	02-2003	Burnham et al.	257/649
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf and Richard N. Tauber, "Silicon Processing for the VLSI Era - Volume 1: Process Technology," Lattice Press, Sunset Beach, California, 1986, pp. 5-6.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.